



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of: **Akira MATSUDA et al.**

Group Art Unit: **1794**

Application Number: **10/719,020**

Examiner: **Michael E. Lavilla**

Filed: **November 24, 2003**

Confirmation Number: **9168**

For: **METHOD FOR PREPARING A CIRCUIT BOARD MATERIAL  
HAVING A CONDUCTIVE BASE AND A RESISTANCE LAYER  
(amended)**

Attorney Docket Number: **032130**

Customer Number: **38834**

**SUBMISSION UNDER 37 C.F.R. §1.114**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

July 2, 2008

Sir:

This Submission is being filed concurrently with a Request for Continued Examination pursuant to 37 C.F.R. §1.114.

**Amendments to the Specification** begin on page 2 of this paper.

**Amendments to the Claims** begin on page 3 of this paper.

**Remarks** begin on page 6 of this paper.